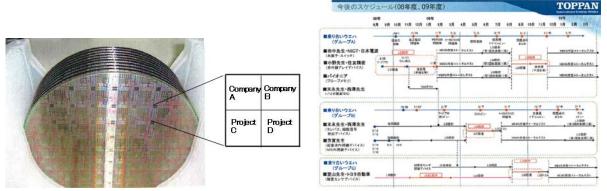
Shared CMOS Wafer (Special Coordination Funds for Promoting Science and Technology, Formation of Innovation Center for Fusion of Advanced Technologies)



Shared CMOS wafer

